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Patent Application

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Serial No.: **09/648164**

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Title: **Architecture for Circuit Connection Of A Vertical Transistor**

**ASSISTANT COMMISSIONER FOR PATENTS**  
**WASHINGTON, D. C. 20231-**

**SIR:**

**Preliminary Amendment**

Please enter the following amendment prior to continued examination of the above-referenced application under 37 CFR 1.114 (Serial Number 09/648164). In response to the Office Action dated, June 27, 2003.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks P.O. Box 1450, Alexandria, VA 22313-1450 on 8/19/03

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